

I-Code SL2 13.56 MHz ISO 15693 RFID IC Read/Write 1024 bit

Features

- 1024 bit EEPROM memory organized in 32 blocks of 4 bytes (*896 bit user memory*)
- 64 bit Unique Serial Number
- Typical operating frequency : 13.56MHz
- ISO 15693 compliant
- EAS (Electronic Article Surveillance) function

Memory Organisation

The 1024 bit EEPROM memory is divided into 32 blocks. A block is the smallest access unit. Each block consists of 4 bytes (1 block = 32 bits). Bit 0 in each byte represents the least significant bit (LSB) and bit 7 the most significant bit (MSB), respectively.

	Byte 0	Byte 1	Byte 2	Byte 3	
Block -4	UID0	UID1	UID2	UID3	Unique Identifier (lower bytes)
Block -3	UID4	UID5	UID6	UID7	Unique Identifier (higher bytes)
Block -2	Internally used	EAS	AFI	DSFID	EAS, AFI, DSFID
Block -1	00	00	00	00	Write Access Conditions
Block 0	x	x	x	x	User Data
Block 1	x	x	x	x	
Block 2	x	x	x	x	
Block 3	x	x	x	x	
Block 4	x	x	x	x	
Block 5	x	x	x	x	
Block 6	x	x	x	x	
Block 7	x	x	x	x	
...	...				
...	...				
...	...				
Block 27	x	x	x	x	User Data

The values (in hexadecimal notation) shown in the table above are stored in the EEPROM after the wafer production process. The contents of blocks marked with 'x' in the table are **not** defined at delivery.

With read and write commands only blocks 0 to 27 can be addressed.

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Sokymat SA, Rte Pra-Charbon 27, 1614 Granges, Switzerland, TEL + 41 (0)21 908 0100, FAX + 41 (0)21 908 0101, EMAIL info@sokymat.ch, WEB www.sokymat.com